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2. Write HDL code to control speed, direction of DC and Stepper motor. 3. Write HDL cod e to accept 8 channel Analog signal, Temperature sensors and display the data on LCD panel or Seven segment display. 4. Write HDL code to generate different waveforms (Sine, Square, Triangle, Ramp etc..) using DAC change the frequency and amplitude. 5.

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VHDL Programming Lab viva questions and answers 1. VHDL means. Very high speed integrated circuit hardware description language. 2. List the types of HDL? 3. Software used in VHDL lab is? 4. Name any two programming tools of VHDL? 5. FPGA means. Field programmable gate array. 6. CPLD means. Complex programmable logic design. 7. ASIC means.

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Generally, the questions that are asked in viva examinations can be grouped under four basic headings: What is it about? What did you do? What did you find? Why does that matter? Practicing how you would answer these four basic questions will take you a long way in your preparations.

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